

## Refine Search

### Search Results -

Term	Documents
SATURAT\$5	0
SATURAT	24
SATURATA	1
SATURATABLE	230
SATURATAD	2
SATURATAED	4
SATURATAES	1
SATURATAION	1
SATURATAL	4
SATURATATED	3
SATURATION	2
(L13 AND SATURAT\$5).PGPB,USPT.	3

There are more results than shown above. [Click here to view the entire set.](#)

**Database:**

US Pre-Grant Publication Full-Text Database  
 US Patents Full-Text Database  
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 EPO Abstracts Database  
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 Derwent World Patents Index  
 IBM Technical Disclosure Bulletins

**Search:**

L16

**Refine Search**

Recall Text

Clear

Interrupt

### Search History

**DATE:** Friday, March 05, 2004   [Printable Copy](#)   [Create Case](#)

**Set**  
**Name**   **Query**  
 side by  
 side

**Hit**  
**Count**   **Set**  
**Name**  
 result set

*DB=PGPB,USPT; PLUR=YES; OP=OR*

<u>L16</u>	l13 and saturat\$5	3	<u>L16</u>
<u>L15</u>	l13 and l4	0	<u>L15</u>
<u>L14</u>	L13 and l5	0	<u>L14</u>
<u>L13</u>	l11 or L12	83	<u>L13</u>
<u>L12</u>	(708/508)[CCLS]	43	<u>L12</u>
<u>L11</u>	(708/521)[CCLS]	44	<u>L11</u>
<u>L10</u>	(708/521)![CCLS]	44	<u>L10</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L9</u>	L8 and pipelin\$7	27	<u>L9</u>
<u>L8</u>	L7 and (second or two or multi or multiple or plur\$7) near5 (field\$1 or operand\$1)	34	<u>L8</u>
<u>L7</u>	l5 and L6	37	<u>L7</u>
<u>L6</u>	l1 or l2	11349	<u>L6</u>
<u>L5</u>	L4 and (pack\$5 or unpack\$5)	149	<u>L5</u>
<u>L4</u>	(dsp or digital near1 processing or application near2 specific or ASIC) and (operand\$1 or instruction\$1) near5 saturat\$4	200	<u>L4</u>
<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>			
<u>L3</u>	(712/10-300)![CCLS]	9292	<u>L3</u>
<u>L2</u>	(708/174-714)[CCLS]	11349	<u>L2</u>
<i>DB=USPT; PLUR=YES; OP=OR</i>			
<u>L1</u>	(708/174-714)![CCLS]	10482	<u>L1</u>

END OF SEARCH HISTORY

## Hit List

Clear

Generate Collection

Print

Fwd Refs

Bkwd Refs

Generate OACS

Search Results - Record(s) 21 through 22 of 22 returned.

☐ 21. Document ID: US 5182811 A

Using default format because multiple data bases are involved.

L11: Entry 21 of 22

File: USPT

Jan 26, 1993

US-PAT-NO: 5182811

DOCUMENT-IDENTIFIER: US 5182811 A

TITLE: Exception, interrupt, and trap handling apparatus which fetches addressing and context data using a single instruction following an interrupt

DATE-ISSUED: January 26, 1993

## INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Sakamura; Ken	Tokyo			JP

US-CL-CURRENT: 710/264

Full	Title	Citation	Front	Review	Classification	Date	Reference	Abstracts	Attachments	Claims	KWIC	Draw De
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☐ 22. Document ID: US 5029069 A

L11: Entry 22 of 22

File: USPT

Jul 2, 1991

US-PAT-NO: 5029069

DOCUMENT-IDENTIFIER: US 5029069 A

**\*\* See image for Certificate of Correction \*\***

TITLE: Data processor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Abstracts	Attachments	Claims	KWIC	Draw De
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Generate OACS

Term	Documents
SPECIFIC	3095808
SPECIFICS	19327

ASIC	32779
ASICS	10614
APPLICATION\$1	0
APPLICATION	4424707
APPLICATIONA	94
APPLICATIONB	11
APPLICATIONC	21
APPLICATIOND	17
APPLICATIONE	20
(L9 AND (APPLICATION\$1 NEAR3 SPECIFIC OR ASIC) AND L7).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	22

There are more results than shown above. Click here to view the entire set.

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**Display Format:**  **Change Format**

[Previous Page](#)

[Next Page](#)

[Go to Doc#](#)

## Refine Search

### Search Results -

Term	Documents
"5021985"	2
5021985S	0
"5021985".PN..PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	2
(5021985.PN.).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	2

Database:

US Pre-Grant Publication Full-Text Database  
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 IBM Technical Disclosure Bulletins

Search:

L13

Refine Search

Recall Text

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Interrupt

### Search History

 DATE: Friday, March 05, 2004    [Printable Copy](#)    [Create Case](#)

<u>Set Name</u> side by side	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u> result set
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L13</u>	5021985.pn.	2	<u>L13</u>
<u>L12</u>	5021985.pn. or 5742780.pn. or 5794067.pn. or 5961630.pn.	6	<u>L12</u>
<u>L11</u>	L9 and (application\$1 near3 specific or ASIC) and l7	22	<u>L11</u>
<u>L10</u>	L9 and (application\$1 near3 specific or ASIC)	22	<u>L10</u>
<u>L9</u>	interrupt\$6 near7 exception\$1 and trap\$4 and (break or break near1 point\$1) near4 instruction\$1 and (debug\$4 or test\$3)	118	<u>L9</u>
<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>			
<u>L8</u>	l2 and L7	126	<u>L8</u>
<u>L7</u>	l2 or l3 or l4 or l5 or L6	12681	<u>L7</u>
<u>L6</u>	(710/261-269)![CCLS]	928	<u>L6</u>



<u>L5</u>	(714/33-41)![CCLS]	2150	<u>L5</u>
<u>L4</u>	(712/17-248)![CCLS]	8621	<u>L4</u>
<u>L3</u>	(717/120-133)![CCLS]	1590	<u>L3</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L2</u>	interrupt\$6 near7 exception\$1 and trap\$4 and (break or break near1 point\$1) near4 instruction\$1	127	<u>L2</u>
<u>L1</u>	interupt\$6 near7 exception\$1 and trap\$4 and (break or break near1 point\$1) near4 instruction\$1	0	<u>L1</u>

END OF SEARCH HISTORY

## Search Forms

## Search Results

## Refine Search

Help

Search Results -

## User Searches

Preferences

Logout

Term	Documents
(17 AND 9).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	1
(L17 AND L9 ).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	1

Database:

US Pre-Grant Publication Full-Text Database  
 US Patents Full-Text Database  
 US OCR Full-Text Database  
 EPO Abstracts Database  
 JPO Abstracts Database  
 Derwent World Patents Index  
 IBM Technical Disclosure Bulletins

Search:

L18

Refine Search

Recall Text

Clear

Interrupt

## Search History

DATE: Friday, March 05, 2004 [Printable Copy](#) [Create Case](#)SetName Queryside by  
sideHit  
CountSet  
Name  
result  
set

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

L18 L17 and l91 L18L17 l14 or l15 or L16812 L17L16 (703/17)[CCLS]169 L16L15 (703/22)[CCLS]408 L15

DB=PGPB,USPT; PLUR=YES; OP=OR

L14 (703/26)![CCLS]277 L14

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

L13 5021985.pn.2 L13L12 5021985.pn, or 5742780.pn. or 5794067.pn. or 5961630.pn.6 L12L11 L9 and (application\$1 near3 specific or ASIC) and l722 L11L10 L9 and (application\$1 near3 specific or ASIC)22 L10

interrupt\$6 near7 exception\$1 and trap\$4 and (break or break near1

<u>L9</u>	point\$1) near4 instruction\$1 and (debug\$4 or test\$3)	118	<u>L9</u>
<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>			
<u>L8</u>	l2 and L7	126	<u>L8</u>
<u>L7</u>	l2 or l3 or l4 or l5 or L6	12681	<u>L7</u>
<u>L6</u>	(710/261-269)! [CCLS]	928	<u>L6</u>
<u>L5</u>	(714/33-41)! [CCLS]	2150	<u>L5</u>
<u>L4</u>	(712/17-248)! [CCLS]	8621	<u>L4</u>
<u>L3</u>	(717/120-133)! [CCLS]	1590	<u>L3</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L2</u>	interrupt\$6 near7 exception\$1 and trap\$4 and (break or break near1 point\$1) near4 instruction\$1	127	<u>L2</u>
<u>L1</u>	interupt\$6 near7 exception\$1 and trap\$4 and (break or break near1 point\$1) near4 instruction\$1	0	<u>L1</u>

END OF SEARCH HISTORY



## Hit List

[Clear](#)[Generate Collection](#)[Print](#)[Fwd Refs](#)[Bkwd Refs](#)[Generate OACS](#)

Search Results - Record(s) 1 through 18 of 18 returned.

☐ 1. Document ID: US 20040034602 A1

Using default format because multiple data bases are involved.

L5: Entry 1 of 18

File: PGPB

Feb 19, 2004

PGPUB-DOCUMENT-NUMBER: 20040034602

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20040034602 A1

TITLE: Method and apparatus for watermarking binary computer code

PUBLICATION-DATE: February 19, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Rubin, Owen	Oakland	CA	US	
Murray, Eric	Los Gatos	CA	US	

US-CL-CURRENT: 705/57

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw. De
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☐ 2. Document ID: US 20040003379 A1

L5: Entry 2 of 18

File: PGPB

Jan 1, 2004

PGPUB-DOCUMENT-NUMBER: 20040003379

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20040003379 A1

TITLE: Compiler, operation processing system and operation processing method

PUBLICATION-DATE: January 1, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Hayashida, Seiji	Kanagawa		JP	

US-CL-CURRENT: 717/140

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw. De
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☐ 3. Document ID: US 20030212712 A1

L5: Entry 3 of 18

File: PGPB

Nov 13, 2003

PGPUB-DOCUMENT-NUMBER: 20030212712  
PGPUB-FILING-TYPE: new  
DOCUMENT-IDENTIFIER: US 20030212712 A1

TITLE: Byte-level file differencing and updating algorithms

PUBLICATION-DATE: November 13, 2003

## INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Gu, Jinsheng	San Mateo	CA	US	
Peng, Luosheng	Santa Clara	CA	US	

US-CL-CURRENT: 707/200

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 4. Document ID: US 20030053473 A1

L5: Entry 4 of 18

File: PGPB

Mar 20, 2003

PGPUB-DOCUMENT-NUMBER: 20030053473  
PGPUB-FILING-TYPE: new  
DOCUMENT-IDENTIFIER: US 20030053473 A1

TITLE: Method and system for processing load distribution for a packetized telecommunication network entity

PUBLICATION-DATE: March 20, 2003

## INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Kung, Lingli	Dallas	TX	US	
Kuan, Chao-Yee	Plano	TX	US	
Tian, Lu	Plano	TX	US	
Lin, Ching-Der	Plano	TX	US	
Naim, Ghassan	Garland	TX	US	
Madhavapeddy, Seshagiri R.	Richardson	TX	US	
Kohli, Pardeep	Plano	TX	US	
Jain, Anuj	Southlake	TX	US	

US-CL-CURRENT: 370/422; 455/433, 718/105

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 5. Document ID: US 20020166113 A1

L5: Entry 5 of 18

File: PGPB

Nov 7, 2002

PGPUB-DOCUMENT-NUMBER: 20020166113  
PGPUB-FILING-TYPE: new  
DOCUMENT-IDENTIFIER: US 20020166113 A1

TITLE: Compiler generation of instruction sequences for unresolved storage references

PUBLICATION-DATE: November 7, 2002

## INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Stoodley, Kevin A.	Richmond Hill		CA	

US-CL-CURRENT: 717/140

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 6. Document ID: US 20020071329 A1

L5: Entry 6 of 18

File: PGPB

Jun 13, 2002

PGPUB-DOCUMENT-NUMBER: 20020071329  
PGPUB-FILING-TYPE: new  
DOCUMENT-IDENTIFIER: US 20020071329 A1

TITLE: DRAM core refresh with reduced spike current

PUBLICATION-DATE: June 13, 2002

## INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Tsern, Ely K.	Los Altos	CA	US	
Barth, Richard M.	Palo Alto	CA	US	
Davis, Paul G.	San Jose	CA	US	
Hampel, Craig E.	San Jose	CA	US	

US-CL-CURRENT: 365/222

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 7. Document ID: US 20020032848 A1

L5: Entry 7 of 18

File: PGPB

Mar 14, 2002

PGPUB-DOCUMENT-NUMBER: 20020032848  
PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020032848 A1

TITLE: Method and apparatus for obtaining a scalar value directly from a vector register

PUBLICATION-DATE: March 14, 2002

## INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Liao, Yu-Chung C.	Austin	TX	US	
Sandon, Peter A.	Essex Junction	VT	US	
Cheng, Howard	Sammamish	WA	US	
Van Hook, Timothy J.	Atherton	CA	US	

US-CL-CURRENT: 712/4; 712/208, 712/222

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 8. Document ID: JP 2002297376 A

L5: Entry 8 of 18

File: DWPI

Oct 11, 2002

DERWENT-ACC-NO: 2003-081150

DERWENT-WEEK: 200308

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TITLE: Programmable controller for ladder sequencing calculation, embeds microcode entry address in operation code of command and directly loads to microcode counter from operation code register

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 9. Document ID: JP 11338708 A

L5: Entry 9 of 18

File: DWPI

Dec 10, 1999

DERWENT-ACC-NO: 2000-211196

DERWENT-WEEK: 200019

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TITLE: Compile procedure for computers, involves inserting process codes for indicating size of operating area and for accessing memory, in loop entrance and code which updates operation area pointer in loop end

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 10. Document ID: WO 9803908 A1, AU 9735160 A, US 5742804 A

L5: Entry 10 of 18

File: DWPI

Jan 29, 1998

DERWENT-ACC-NO: 1998-120964  
DERWENT-WEEK: 199827  
COPYRIGHT 2004 DERWENT INFORMATION LTD

TITLE: Reducing instruction fetch penalty in instruction sequence execution -  
inserting into programmed sequence branch predict instruction with op-code  
specifying branch as likely to be taken or not, and target address of branch, this  
instruction otherwise does not affect architectural state of microprocessor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWMC	Draw. De
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☐ 11. Document ID: US 5255382 A

L5: Entry 11 of 18

File: DWPI

Oct 19, 1993

DERWENT-ACC-NO: 1993-345329  
DERWENT-WEEK: 199343  
COPYRIGHT 2004 DERWENT INFORMATION LTD

TITLE: 8051-based microcontroller system with program memory expander - increments  
or decrements contents of extended address register in conjunction with  
incrementing or decrementing by 8051 microcontroller of its program counter

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWMC	Draw. De
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☐ 12. Document ID: JP 04054042 A

L5: Entry 12 of 18

File: DWPI

Feb 21, 1992

DERWENT-ACC-NO: 1992-186585  
DERWENT-WEEK: 199223  
COPYRIGHT 2004 DERWENT INFORMATION LTD

TITLE: Transmission signal control system for data communication equipment -  
inserts operation control code into free bit location of input signal NoAbstract  
Dwg 1/2

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWMC	Draw. De
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☐ 13. Document ID: IT 1213382 B

L5: Entry 13 of 18

File: DWPI

Dec 20, 1989

DERWENT-ACC-NO: 1991-371692  
DERWENT-WEEK: 199151  
COPYRIGHT 2004 DERWENT INFORMATION LTD

TITLE: Electronic keyboard type insert for e.g. telecommunications - is located at  
use point for coded programmed operation NoAbstract

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWMC	Draw. De
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☐ 14. Document ID: GB 2197506 A, DE 3732808 A, FR 2606903 A, US 4866665 A

L5: Entry 14 of 18

File: DWPI

May 18, 1988

DERWENT-ACC-NO: 1988-135063

DERWENT-WEEK: 198820

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TITLE: Monitoring method for execution of user program - defining software break points at desired addresses by replacing user instructions with instructions for calling monitor routine

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 15. Document ID: US 3537074 A

L5: Entry 15 of 18

File: USOC

Oct 27, 1970

US-PAT-NO: 3537074

DOCUMENT-IDENTIFIER: US 3537074 A

TITLE: PARALLEL OPERATING ARRAY COMPUTER

DATE-ISSUED: October 27, 1970

INVENTOR-NAME: SANKIN ALBERT; BARNES GEORGE H ; STOKES RICHARD A

US-CL-CURRENT: 712/14

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 16. Document ID: US 3533084 A

L5: Entry 16 of 18

File: USOC

Oct 6, 1970

US-PAT-NO: 3533084

DOCUMENT-IDENTIFIER: US 3533084 A

TITLE: SPACE RESERVATION AND TICKET PRINTING SYSTEM

DATE-ISSUED: October 6, 1970

INVENTOR-NAME: COOK ROBERT D; MCHALE WALTER T

US-CL-CURRENT: 340/825.29

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 17. Document ID: US 3444521 A

L5: Entry 17 of 18

File: USOC

May 13, 1969

US-PAT-NO: 3444521

DOCUMENT-IDENTIFIER: US 3444521 A

TITLE: SUPERVISORY CONTROL SYSTEM COMBINING SCANNING AND DIRECT SELECTION MODES OF OPERATION

DATE-ISSUED: May 13, 1969

INVENTOR-NAME: BREESE LEMUEL R

US-CL-CURRENT: 340/3.52; 340/10.32, 340/3.4, 340/3.51, 340/3.54, 340/3.63, 340/3.7,  
340/825.26, 340/825.37

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 18. Document ID: US 3142043 A

L5: Entry 18 of 18

File: USOC

Jul 21, 1964

US-PAT-NO: 3142043

DOCUMENT-IDENTIFIER: US 3142043 A

TITLE: Information handling apparatus for distributing data in a storage apparatus

DATE-ISSUED: July 21, 1964

INVENTOR-NAME: SCHRIMPF HENRY W

US-CL-CURRENT: 710/4; 710/3

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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Term	Documents
(4 NOT 3).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	18
(L4 NOT L3).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	18

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